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Fig. 1

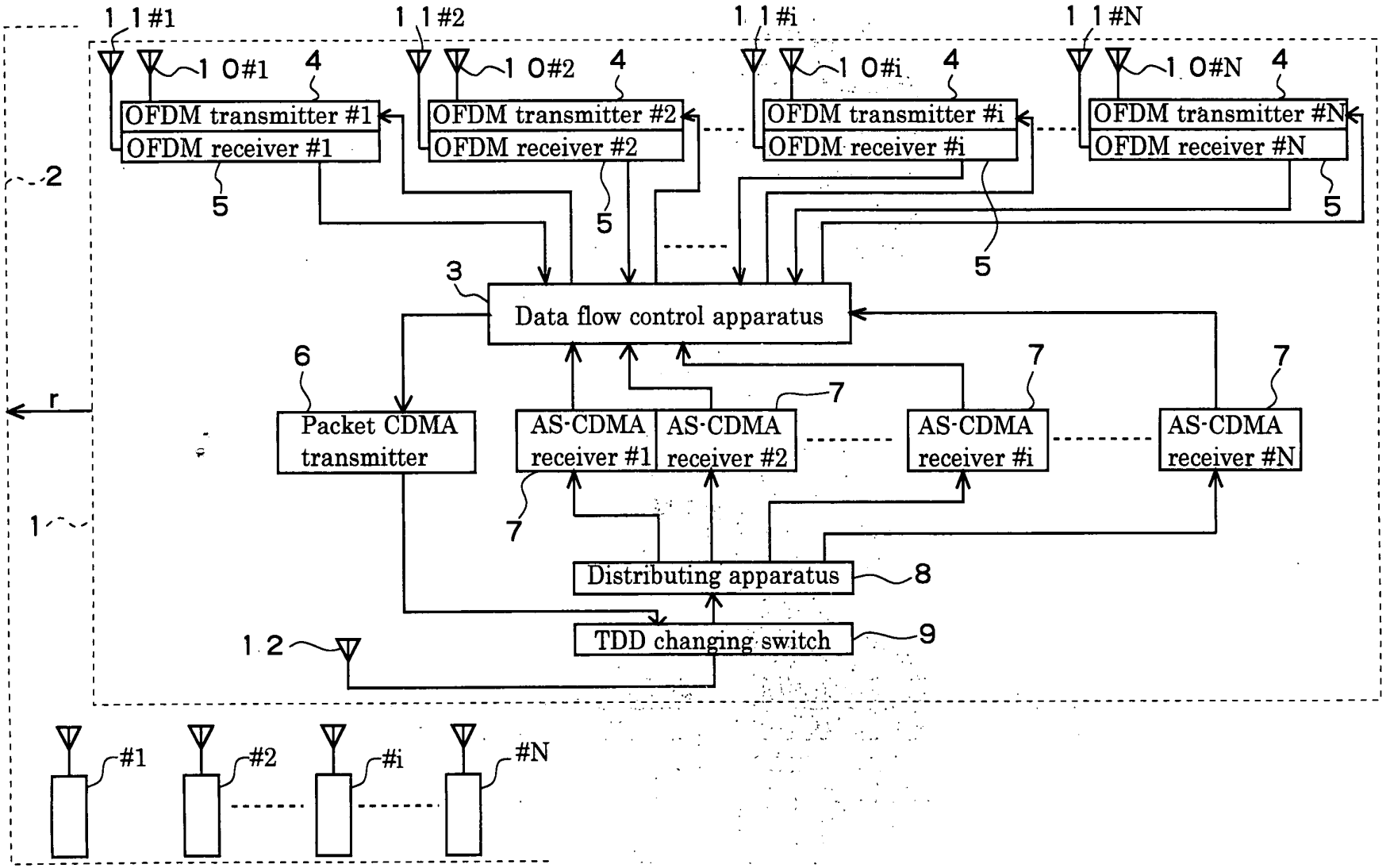

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Fig. 2

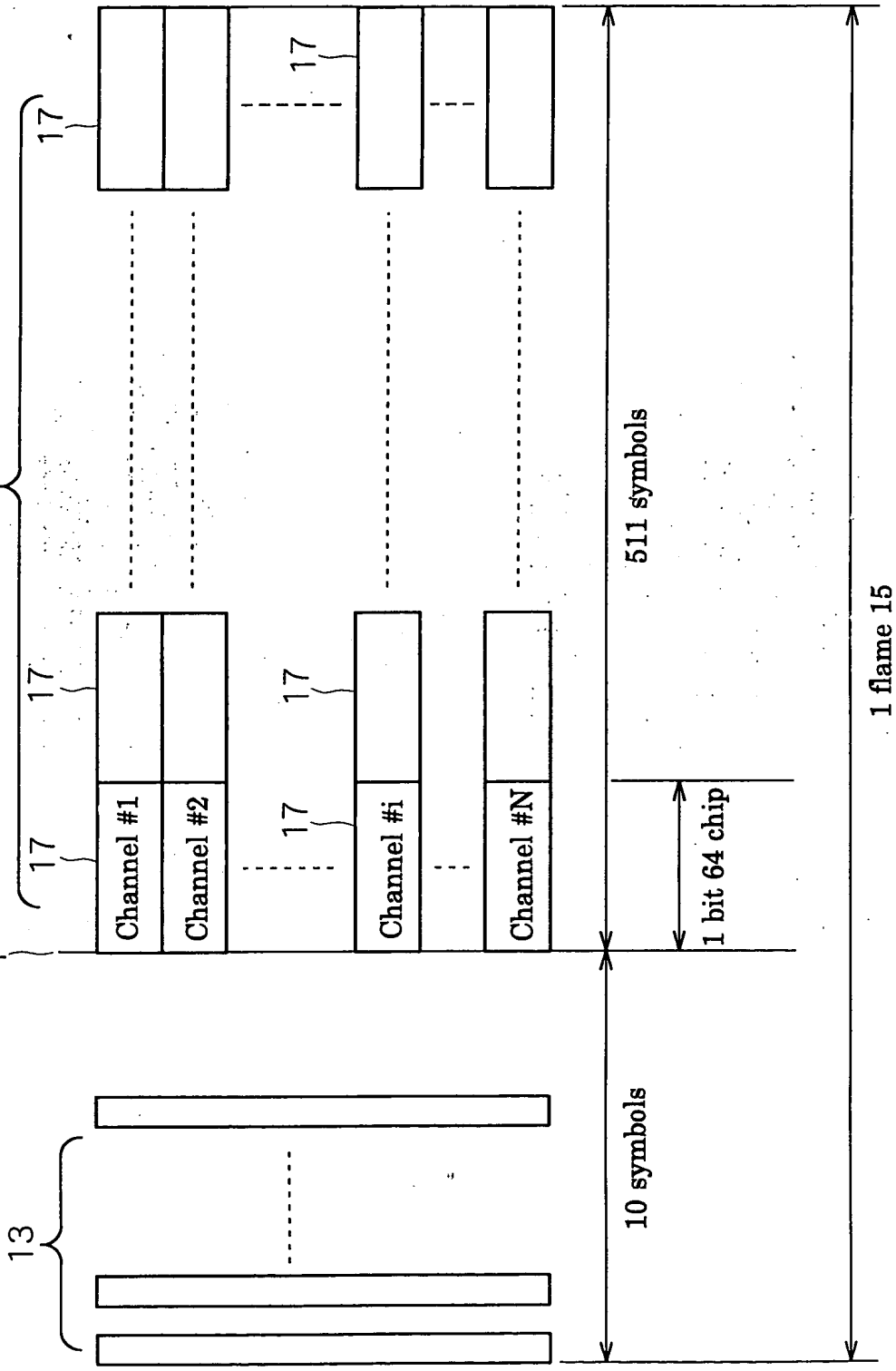

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Fig. 3

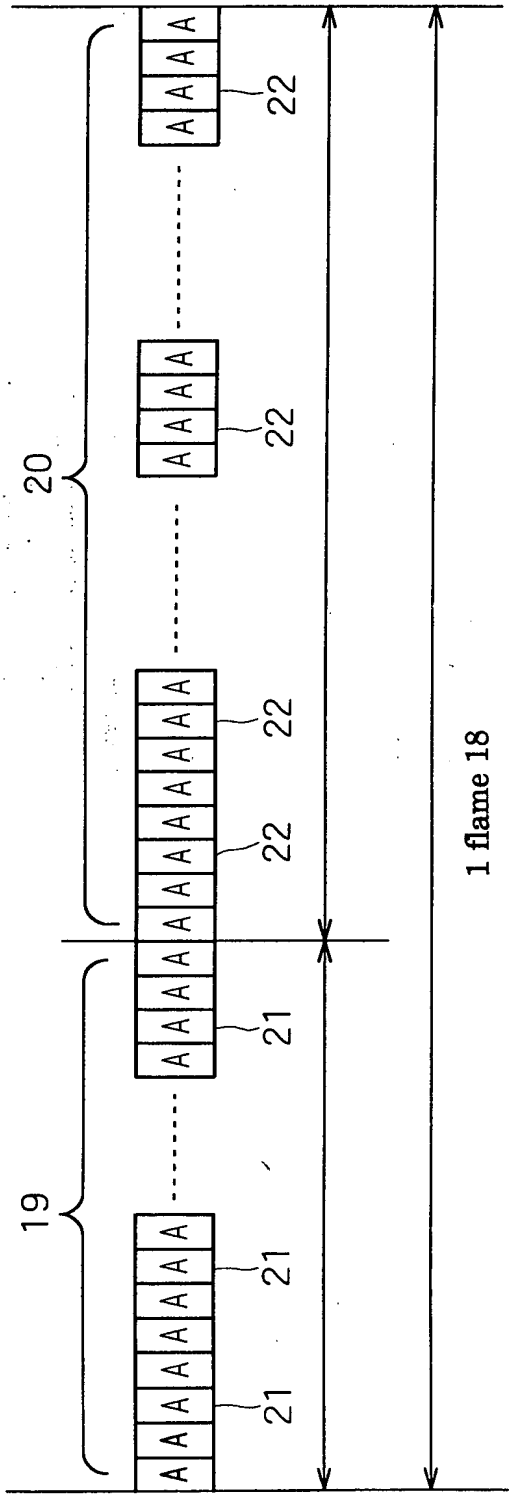

Fig. 4

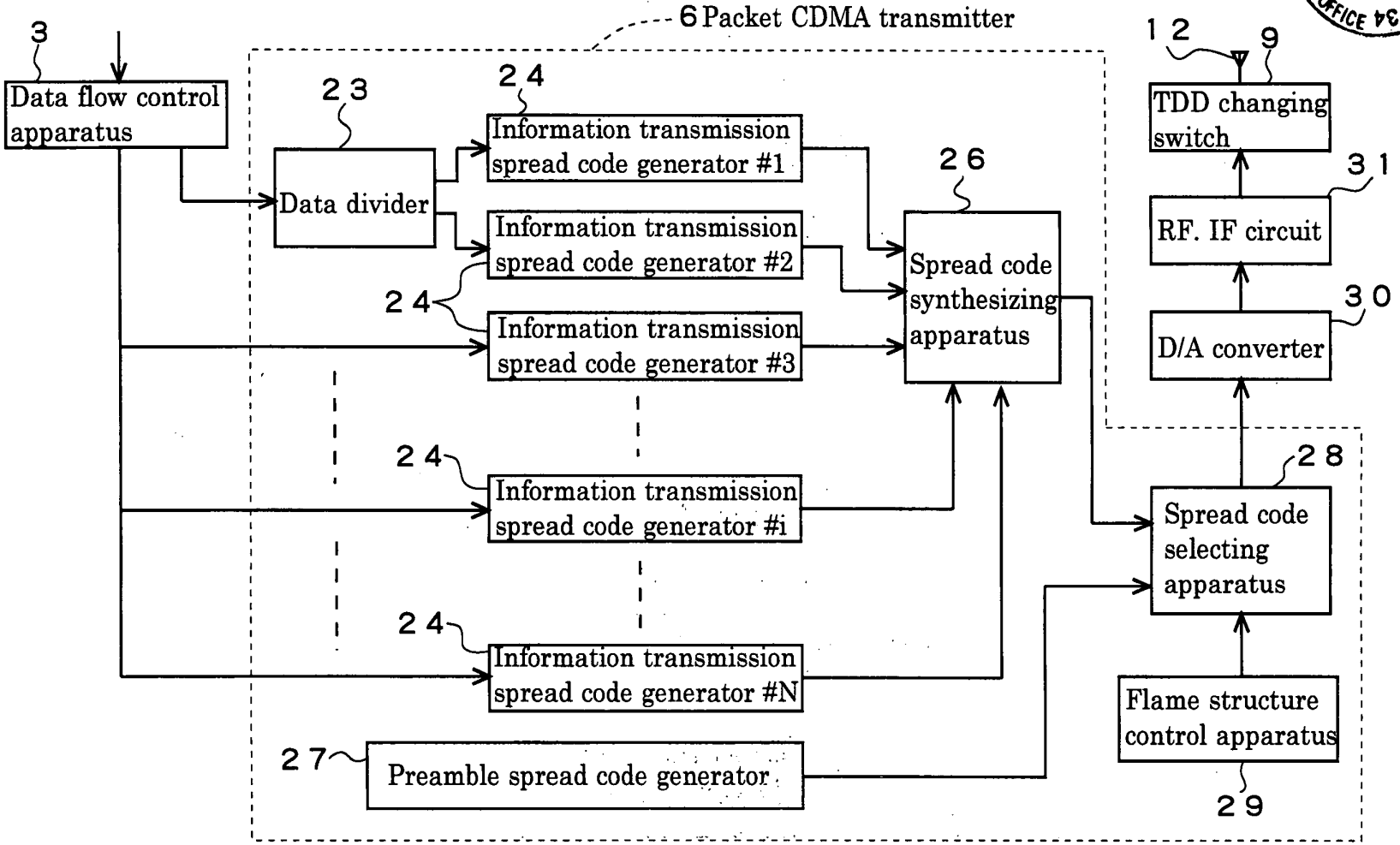

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Fig. 5

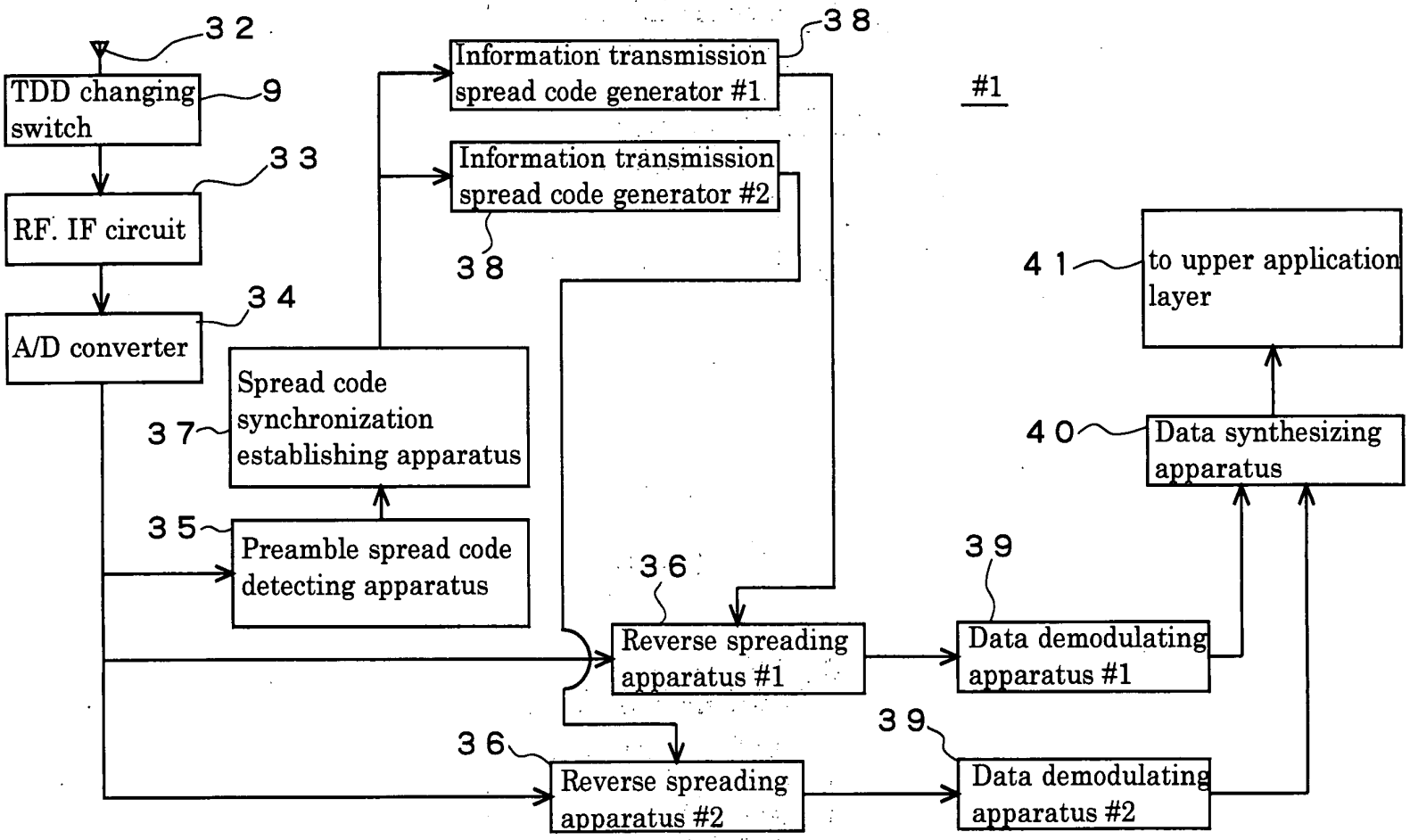

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Fig. 6

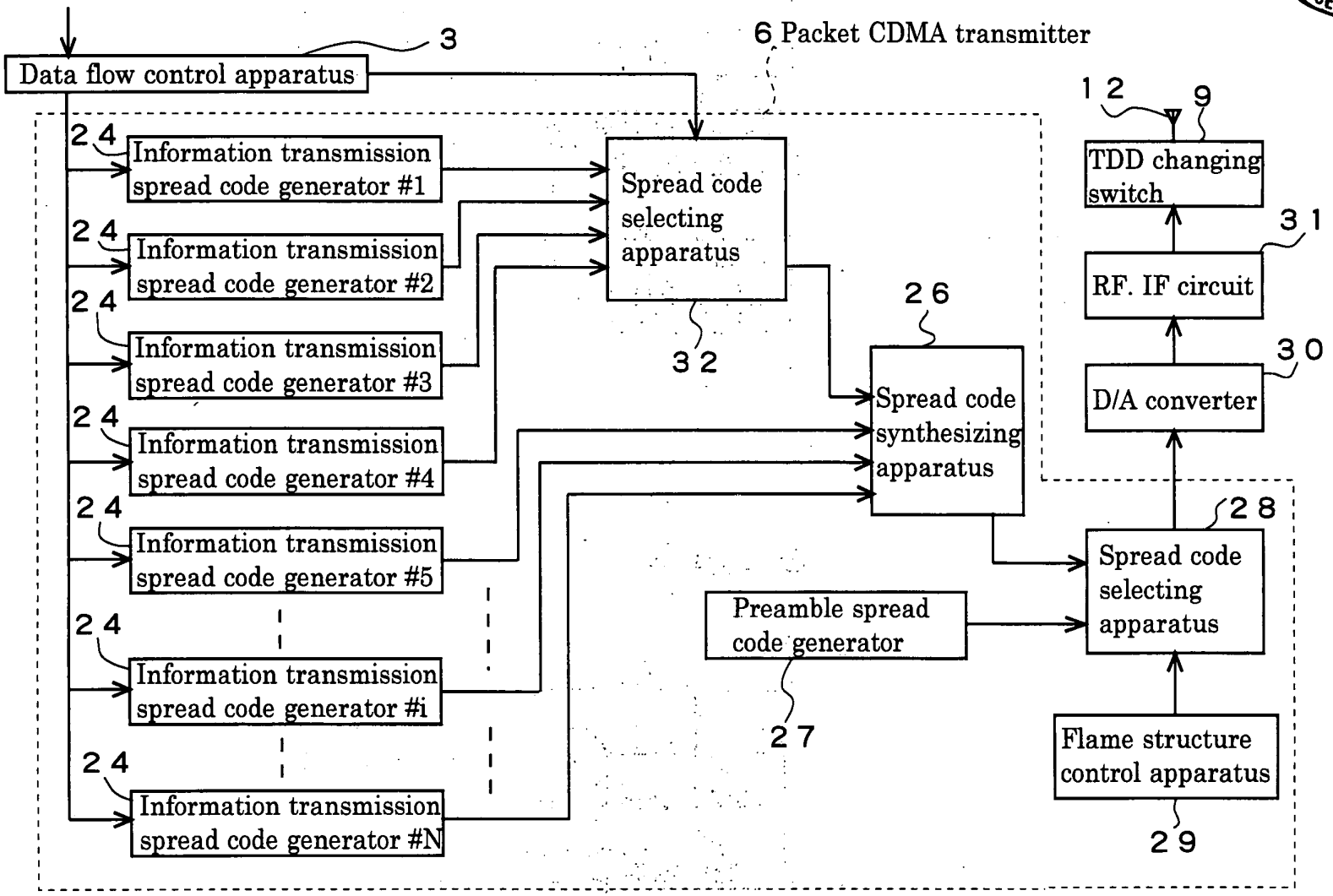

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Fig. 7

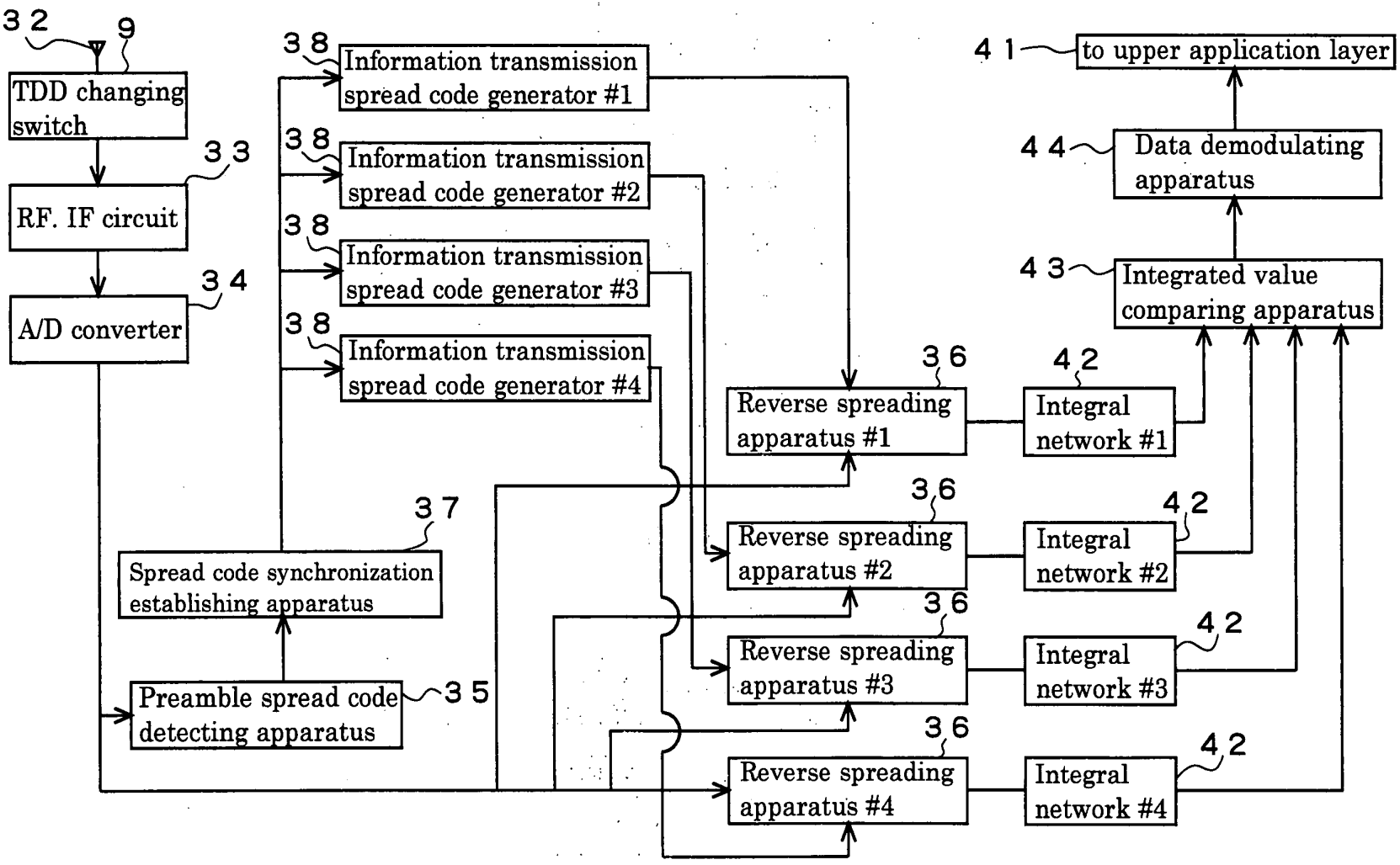

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Fig. 8

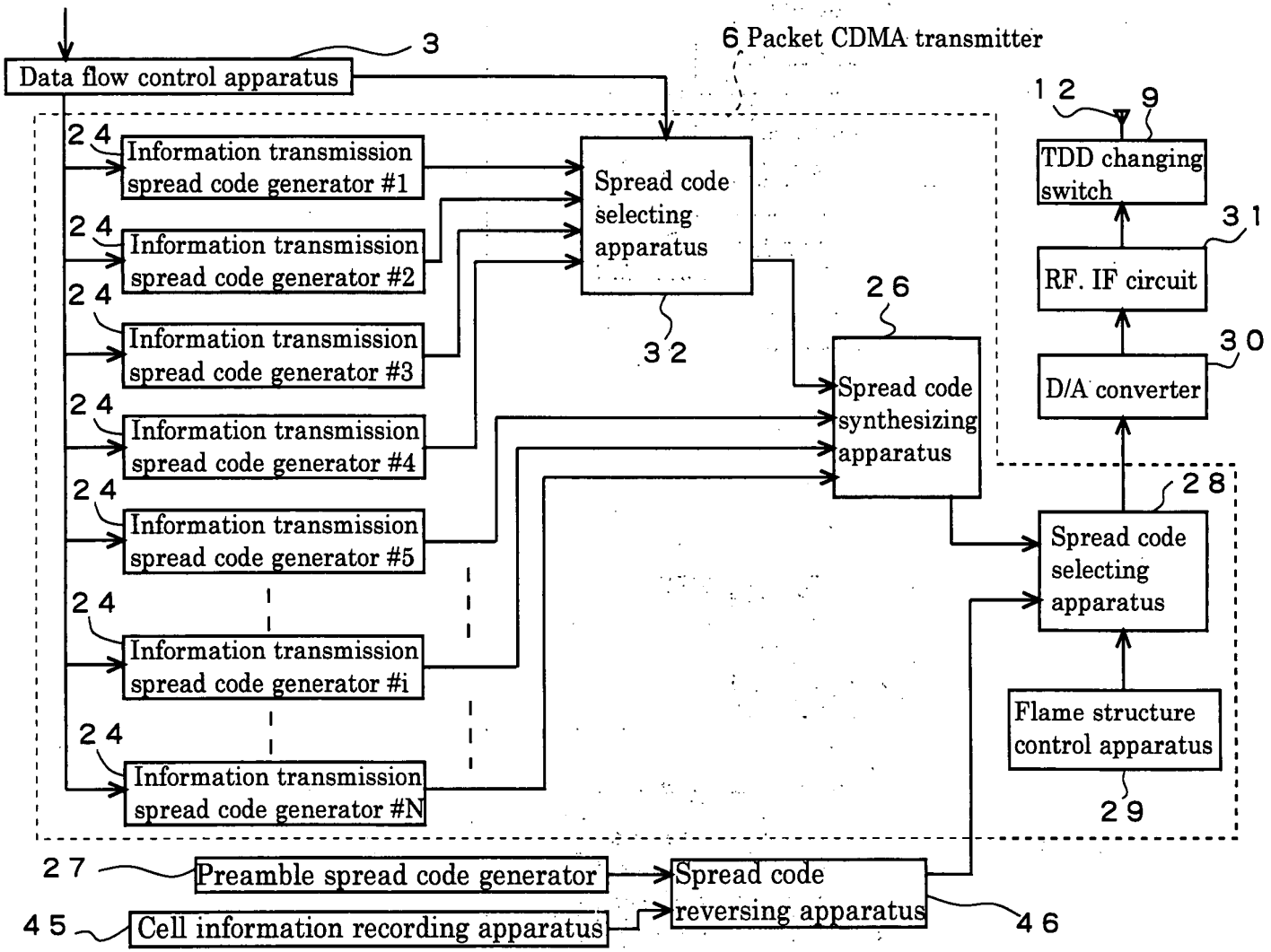

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Fig. 9

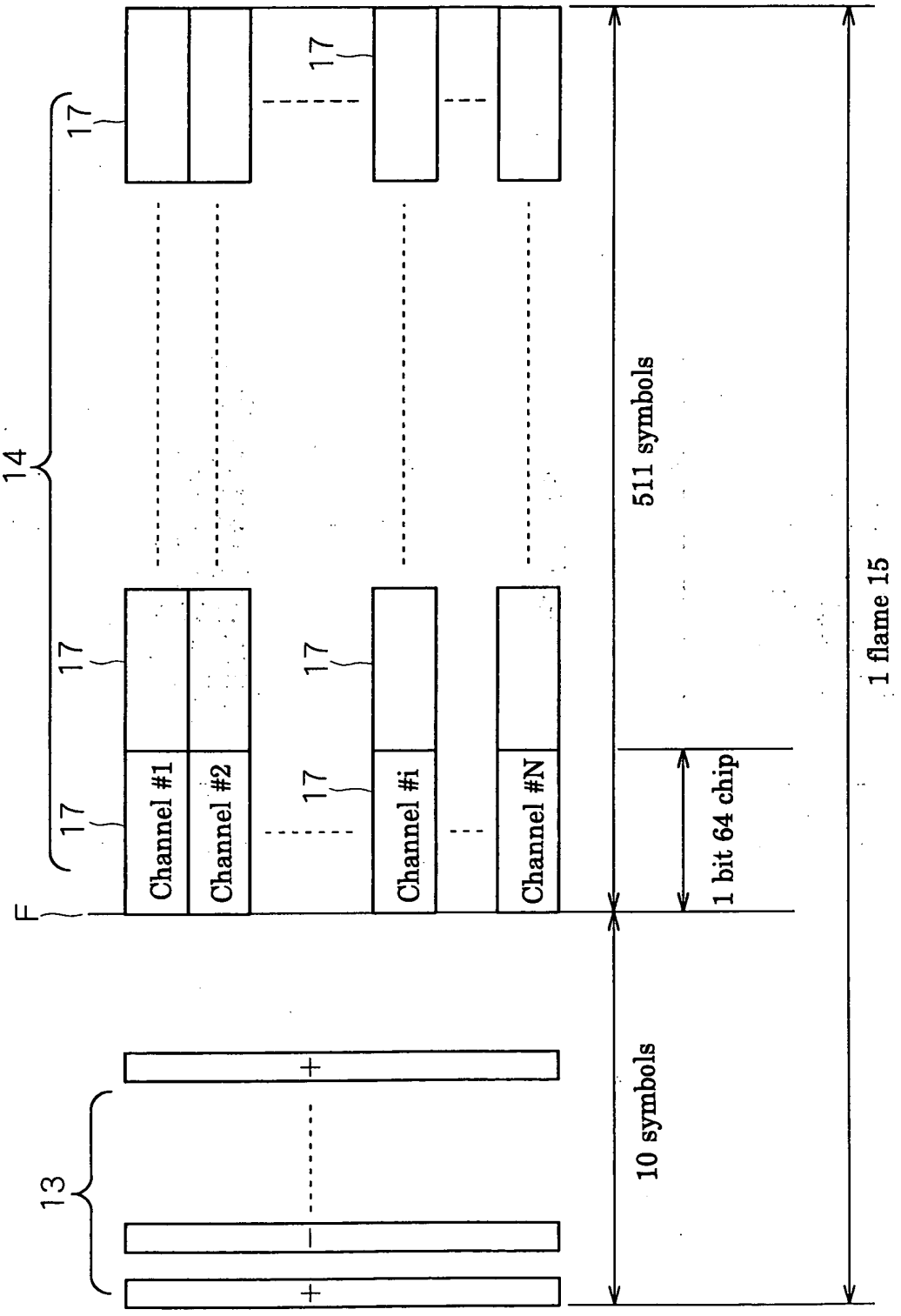

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Fig. 10

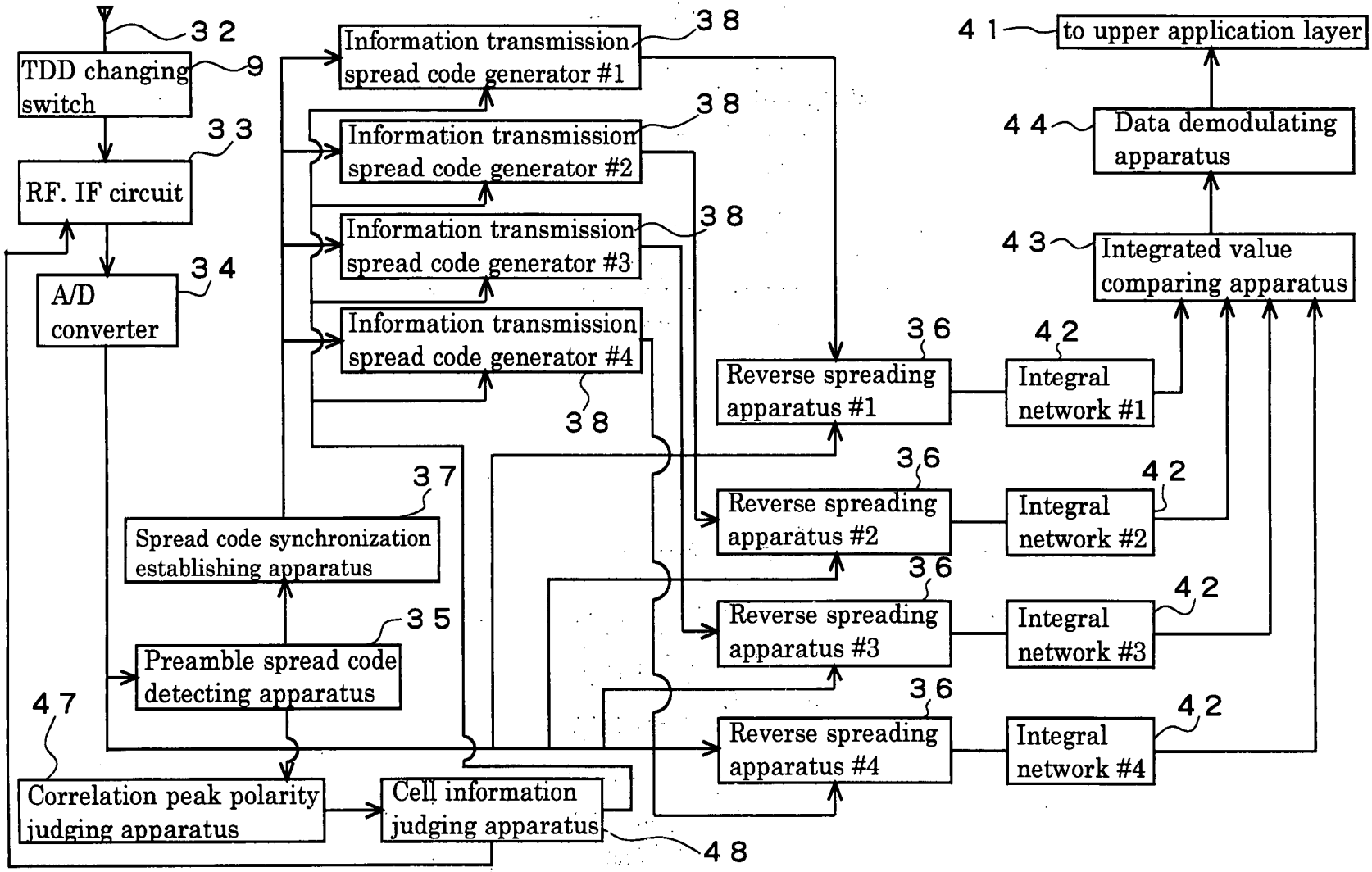

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Fig. 11

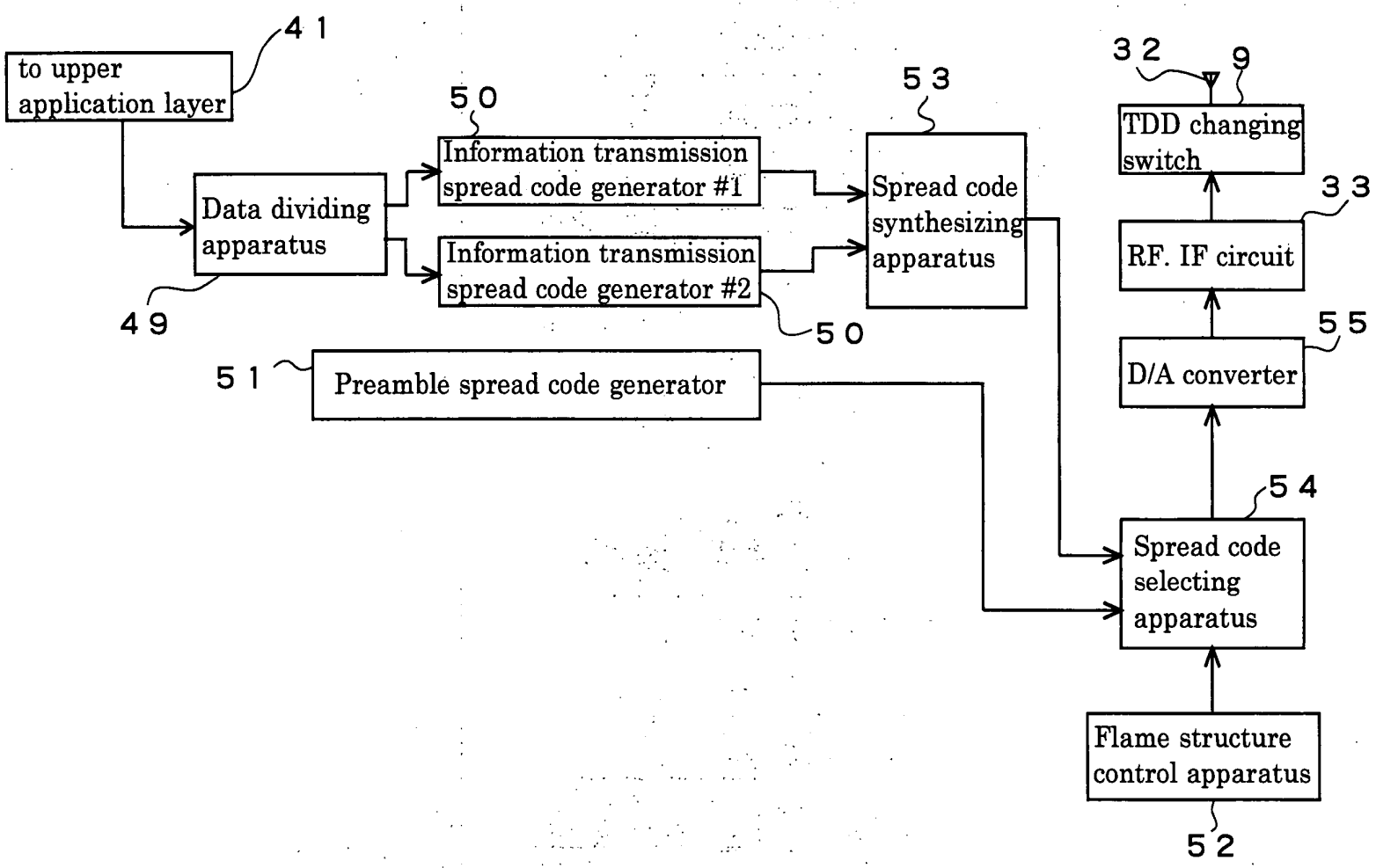

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Fig. 12(a)

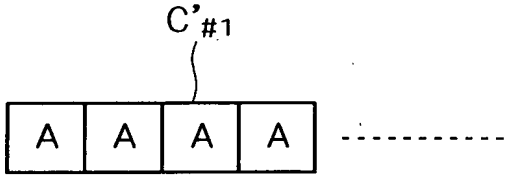

Fig. 12(b)

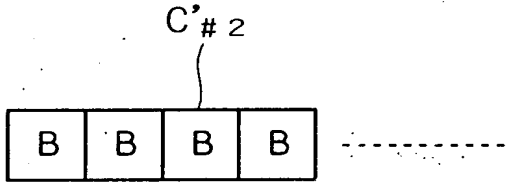

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Fig. 13

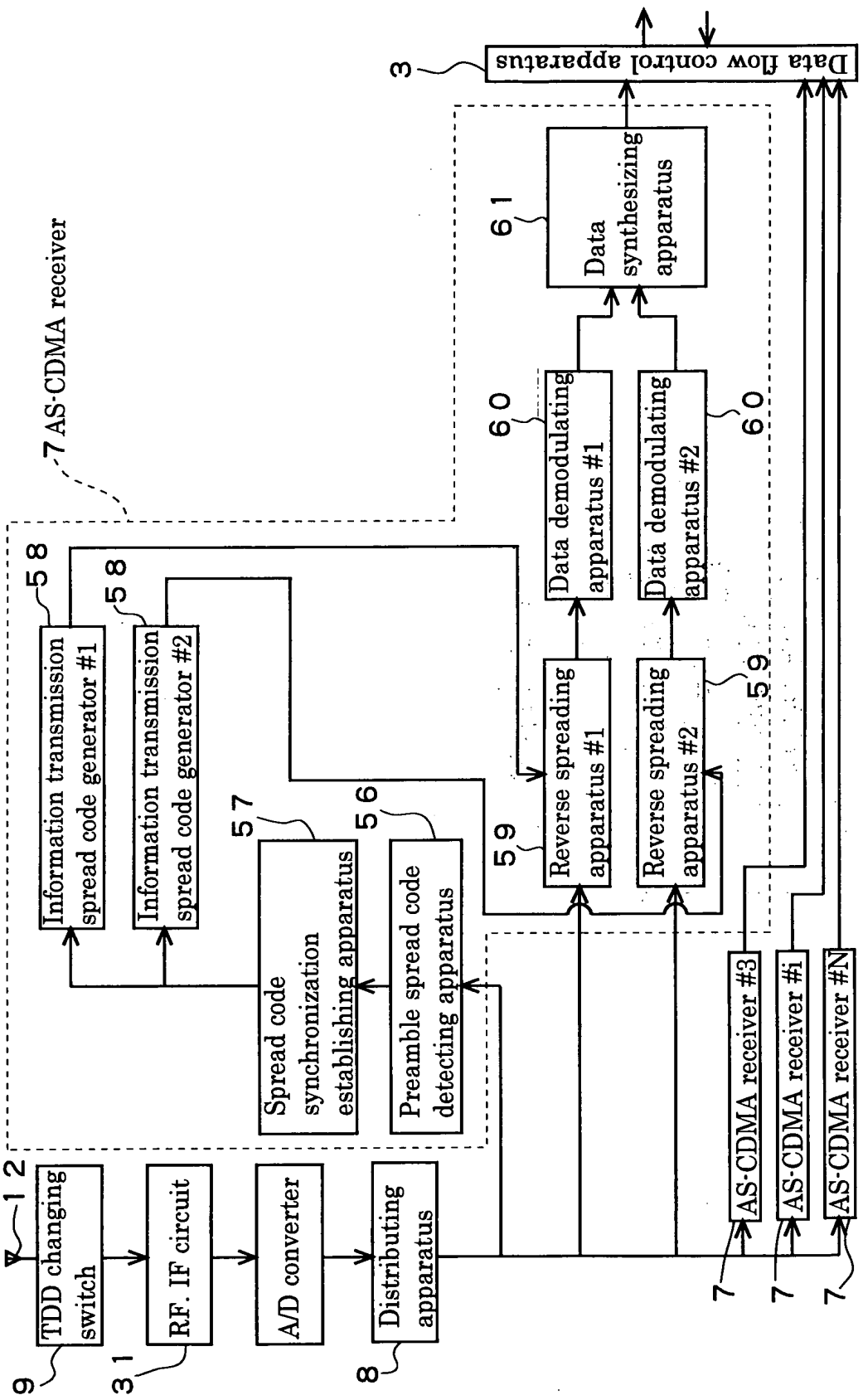

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Fig. 14

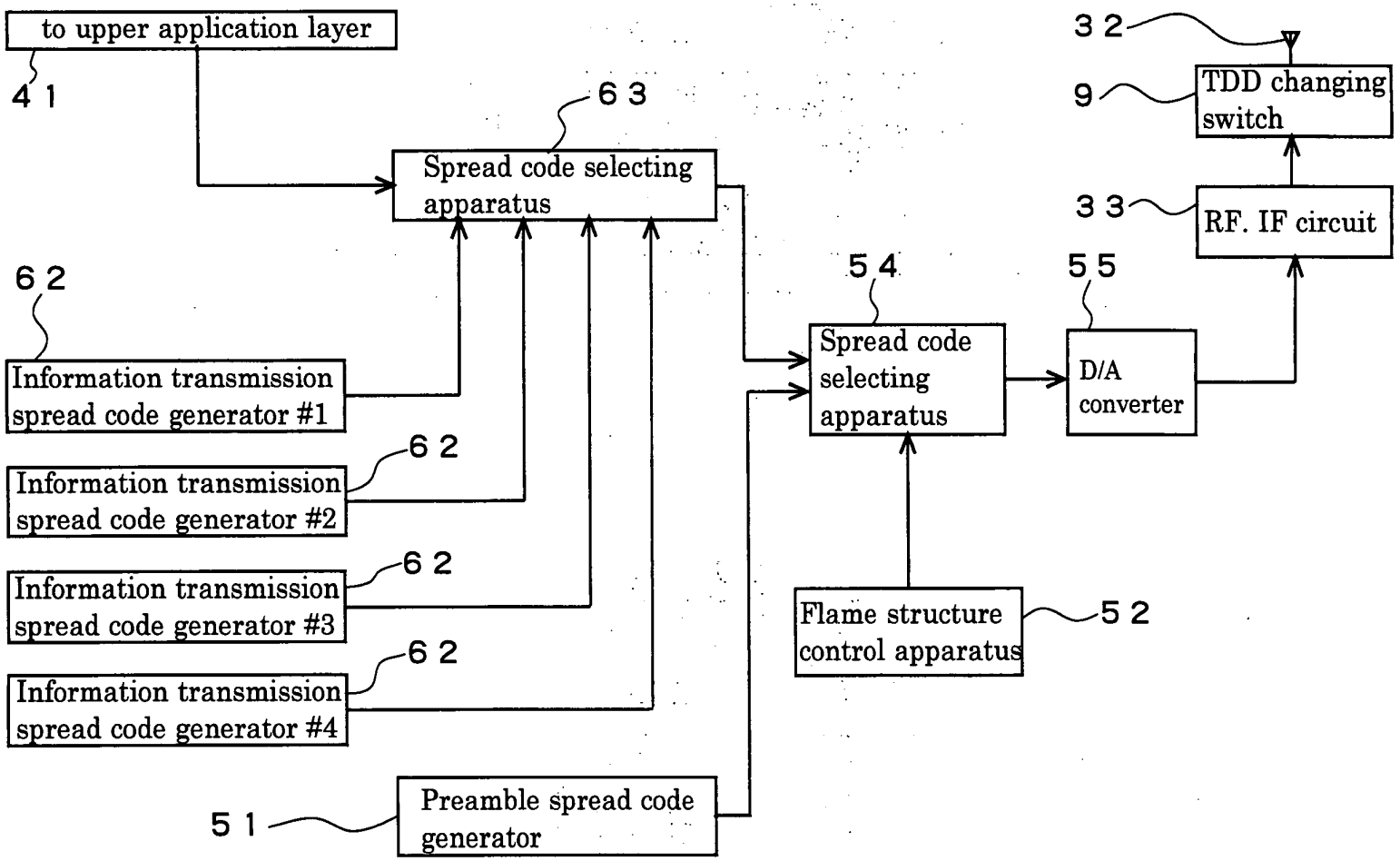

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Fig. 15

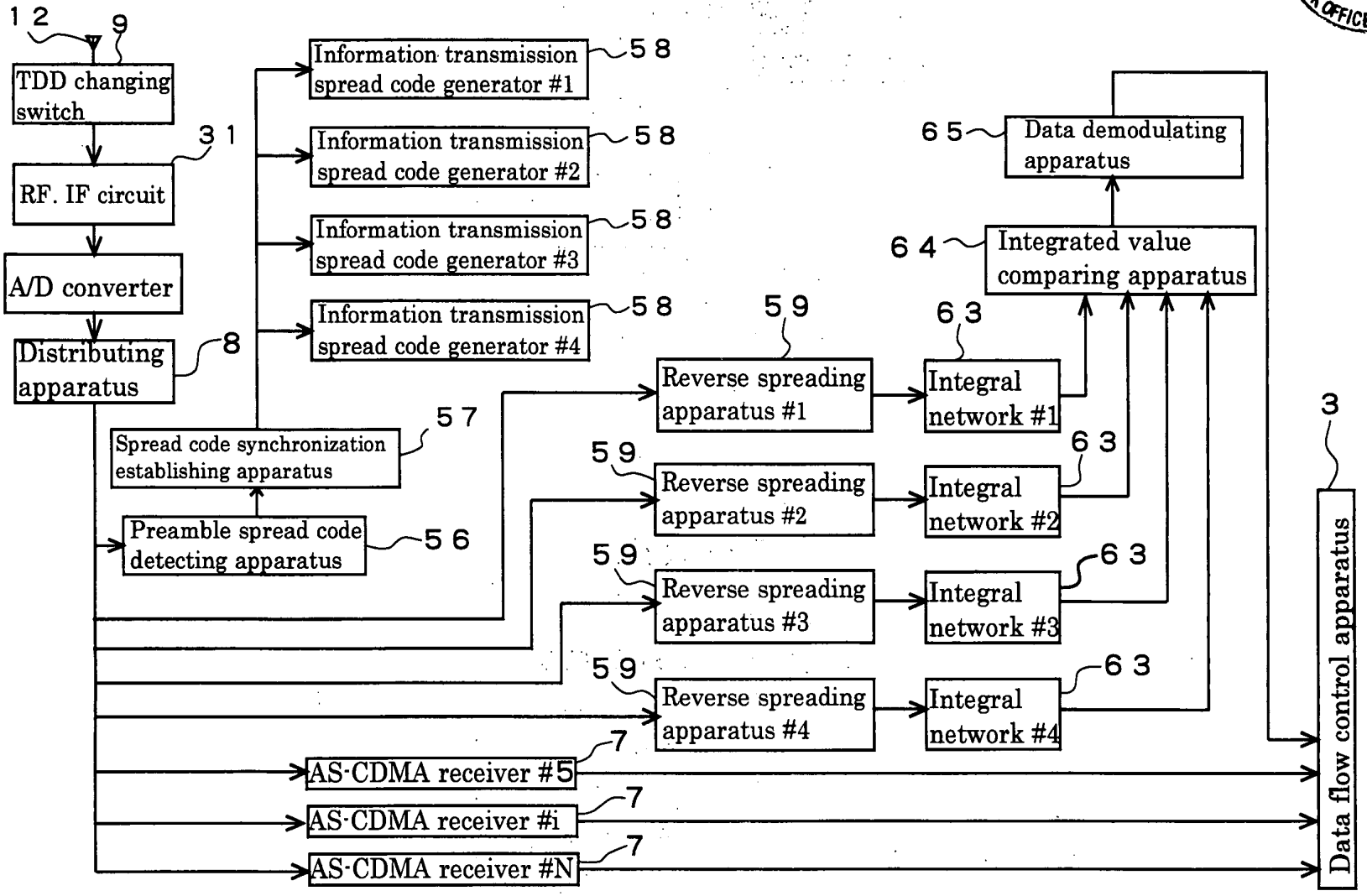

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Fig. 16

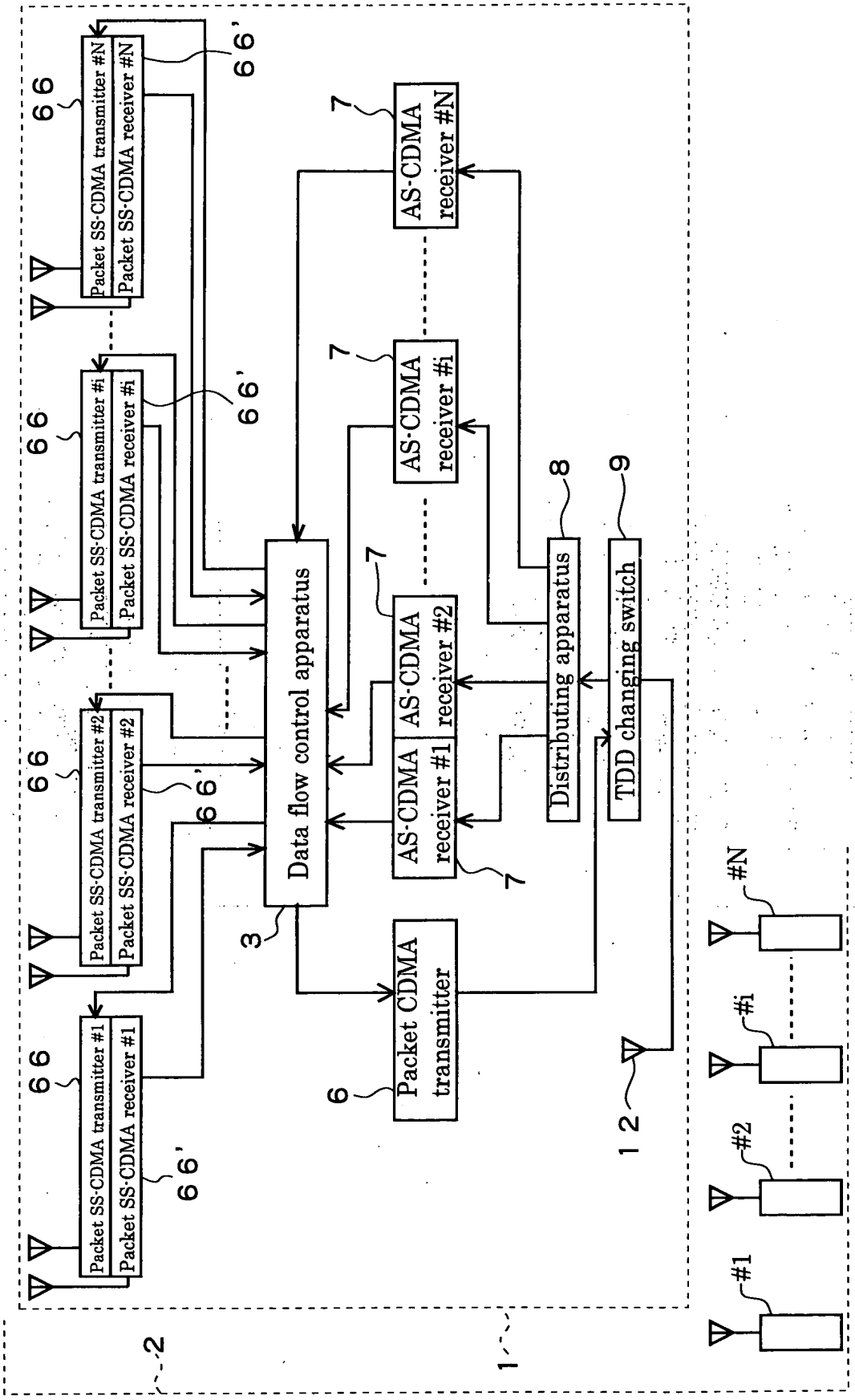

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Fig. 17(a)

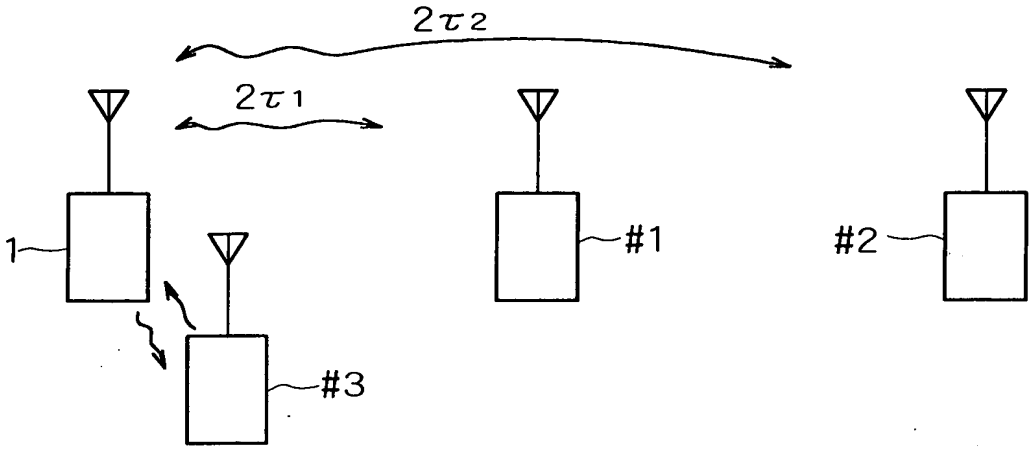

Fig. 17(b)

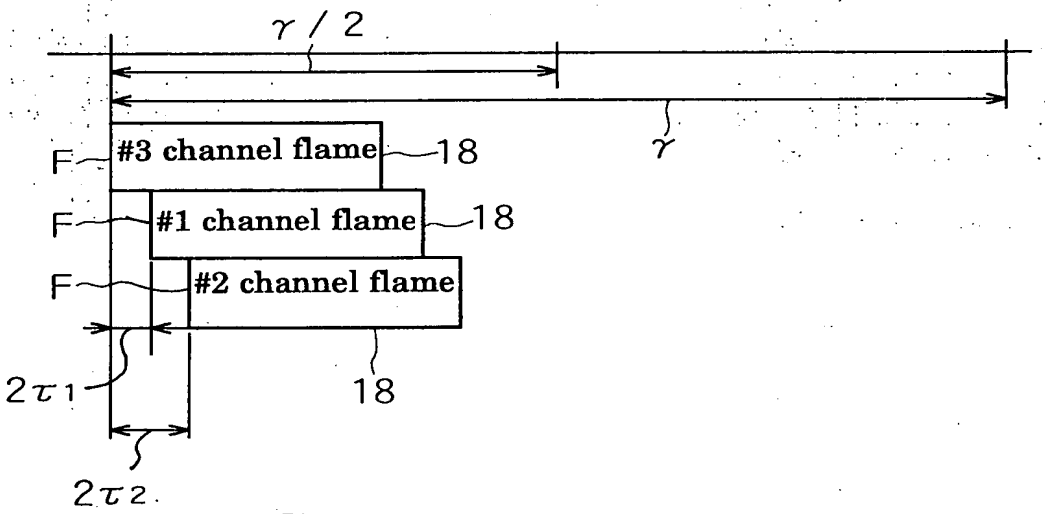

Fig. 17(c)

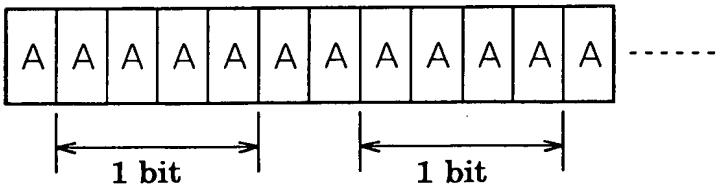

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Fig. 18

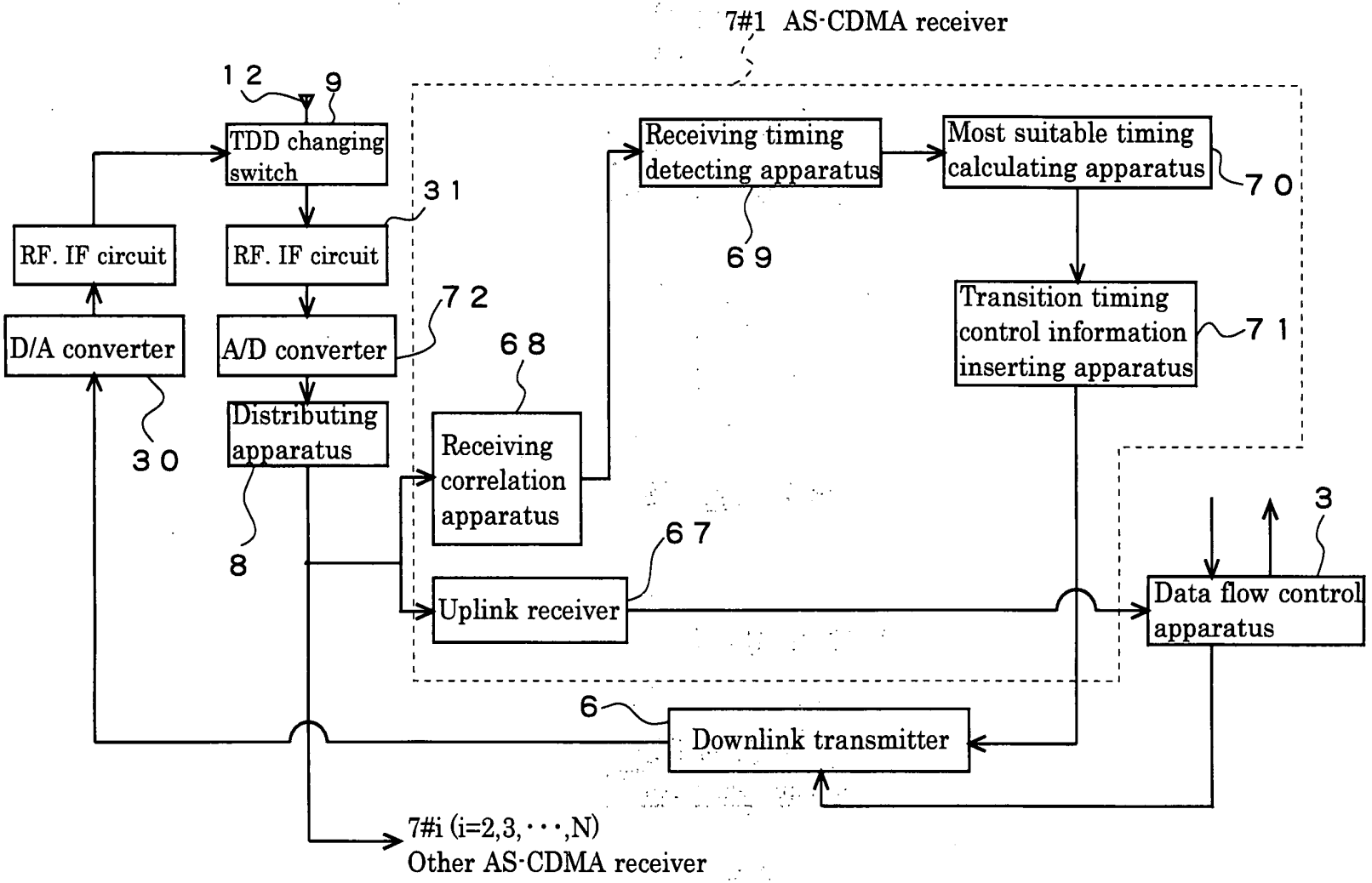

Fig. 19

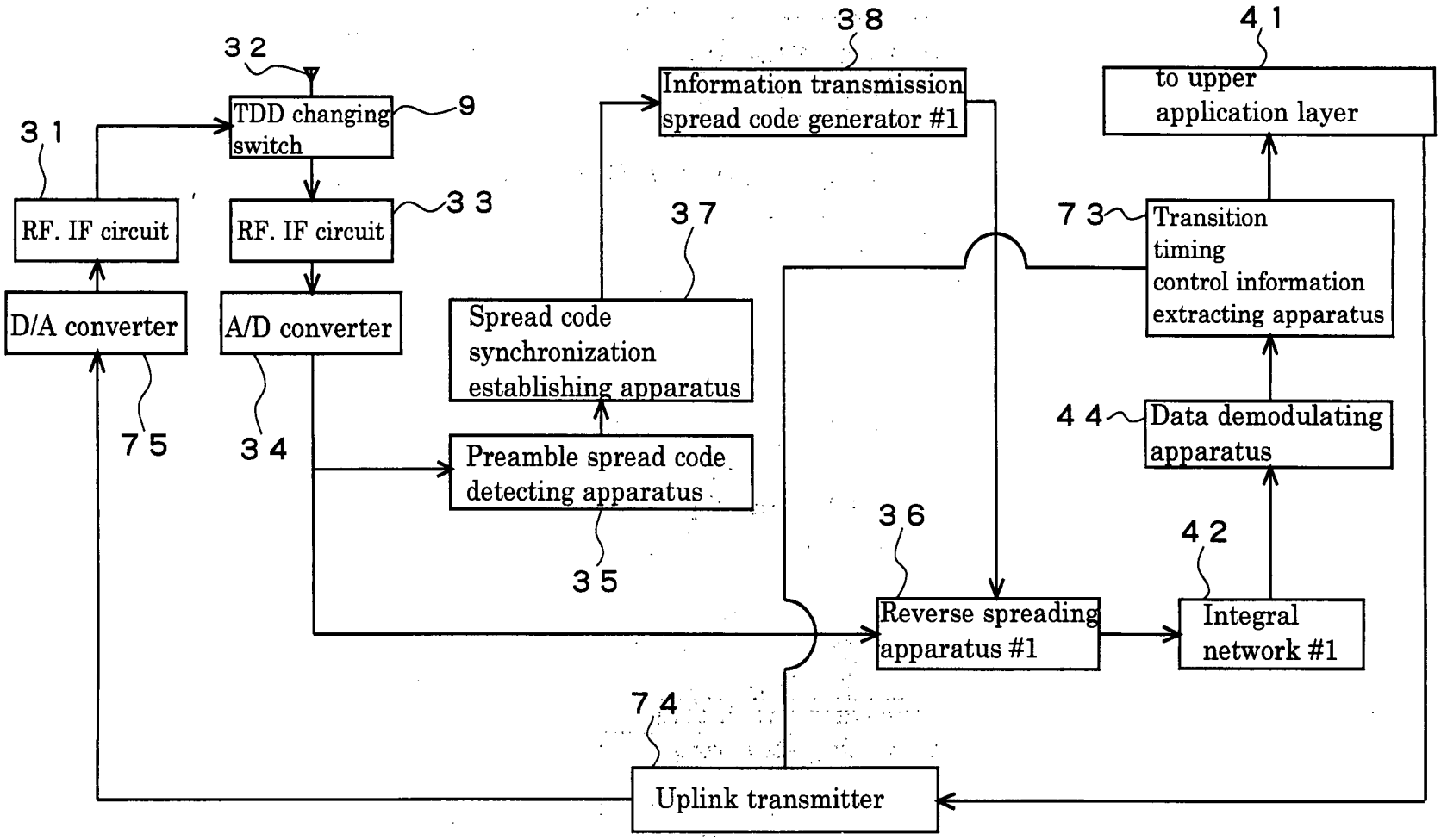
32
38
41
TDD changing switch
9
Information transmission spread code generator #1
to upper application layer
31
RF. IF circuit
RF. IF circuit
33
37
73
Transition timing control information extracting apparatus
D/A converter
A/D converter
Spread code synchronization establishing apparatus
44
Data demodulating apparatus
75
34
Preamble spread code detecting apparatus
36
42
35
Reverse spreading apparatus #1
Integral network #1
74
Uplink transmitter

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Fig. 20(a)

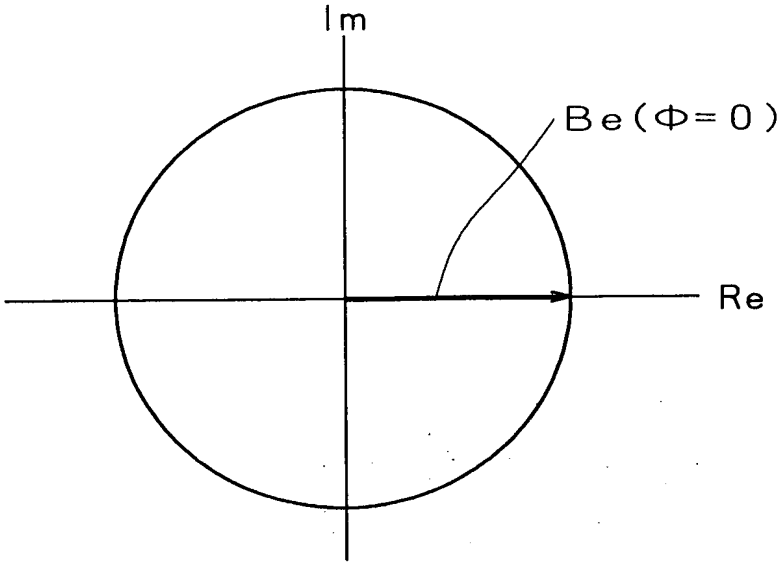


Fig. 20(b)

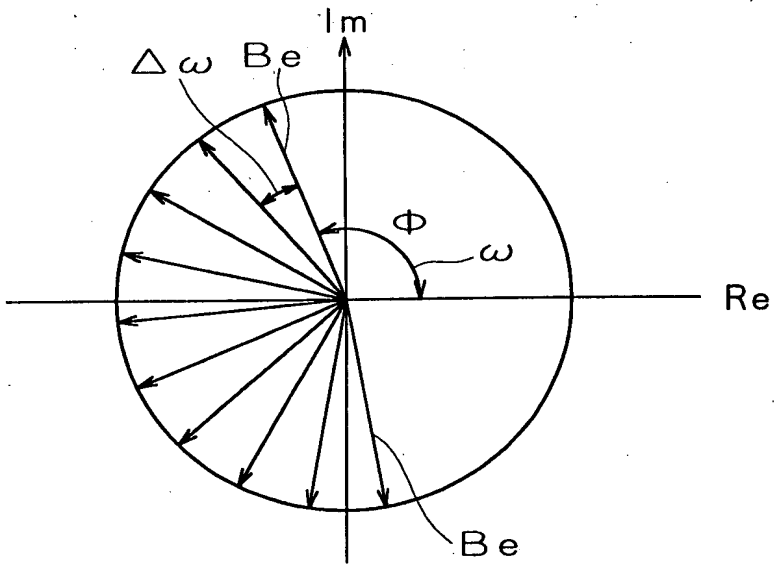
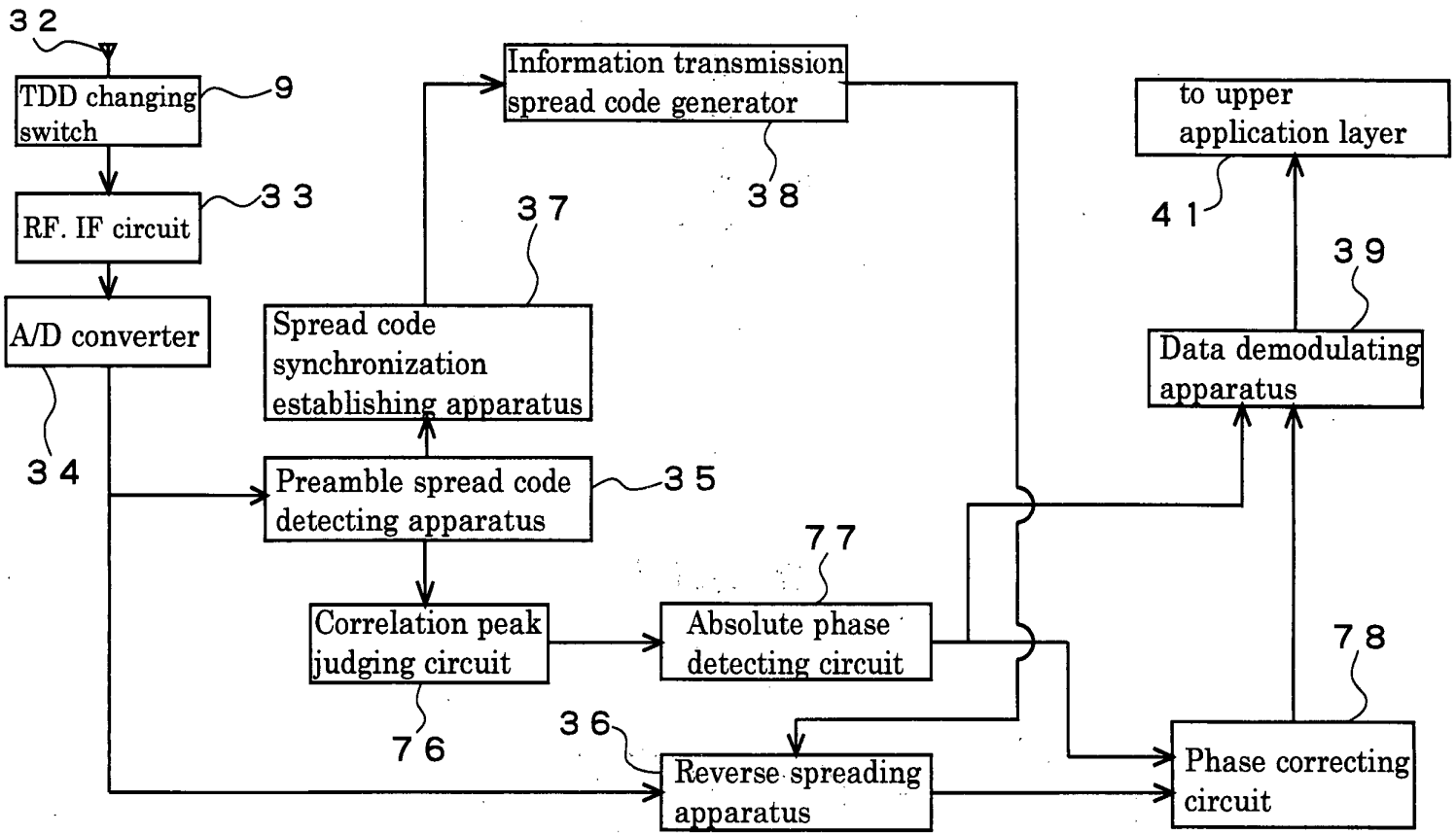


Fig. 21


32
TDD changing switch
9
RF. IF circuit
33
A/D converter
34
Spread code synchronization establishing apparatus
37
Information transmission spread code generator
38
Preamble spread code detecting apparatus
35
Correlation peak judging circuit
76
Absolute phase detecting circuit
77
36
Reverse spreading apparatus
to upper application layer
41
Data demodulating apparatus
39
Phase correcting circuit
78